

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2001-148692

(43)Date of publication of application : 29.05.2001

(51)Int.Cl. H04L 7/033
H04L 7/08
H04L 7/10
// G06F 1/12

(21)Application number : 2000-256828 (71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

(22)Date of filing : 28.08.2000 (72)Inventor : YOSHIKAWA TAKEFUMI

(30)Priority

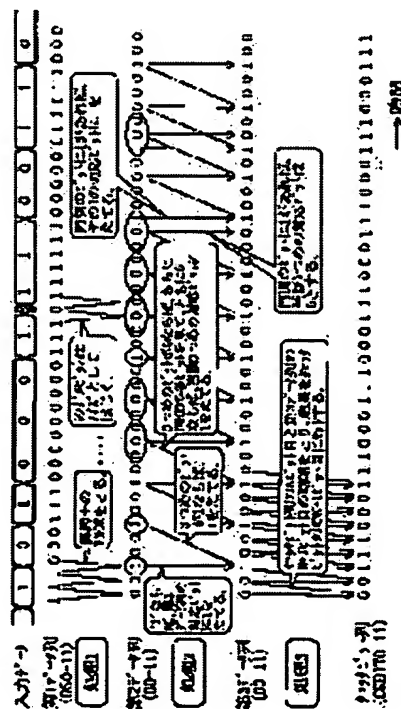
Priority number : 11255030 Priority date : 09.09.1999 Priority country : JP

(54) CLOCK EXTRACTOR

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a clock extractor which can extract the clocks corresponding to plural input ports by means of a phase-locked loop(PLL) and also can improve the data transmission efficiency by eliminating the preamble period that is conventionally necessary for the phase adjustment when the burst data are transmitted.

SOLUTION: The serial data to be inputted are quantized via the triple over sampling to obtain a 1st data string, and a 2nd data string that specifies a changing point of the 1st data string is produced from the 1st data string via the EXOR processes which are adjacent to each other. The 3rd bit and its preceding and next bits are referred to at each changing point of the 2nd data string, and the 3rd bit is defined as a boundary point when the said preceding and next bit have no changing points. When the preceding and next bits have changing points, these bits are



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defined as boundary points. Thus, a 3rd data string is produced. Then a final clock bit string is produced by carrying out the time series EXOR between the 3rd data string and a clock bit string.

LEGAL STATUS

[Date of request for examination] 28.08.2000

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3389560

[Date of registration] 17.01.2003

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

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